

IN THE SPECIFICATION

Please amend the specification as detailed below.

On Page 10, lines 16-20, please replace the paragraph with the following:

Having visual “on” and “off” states 212 and 214 within active state (S0) 202 is a non-essential aspect of the disclosed embodiments of the present invention. The feature is the subject matter of co-pending U.S. Patent Application, number ~~<to be inserted>~~ 10/644,978, entitled ~~<insert title>~~ Method, Apparatus And System For Operating Data Processing Devices, and filed ~~on mm/dd/yy~~ contemporaneously. For further details, see the co-pending application.

On Page 11, lines 12-20, please replace the paragraph with the following:

From “suspended to memory with a persistent copy of the system state saved” state **218**, system **100** may enter un-powered state (G3) **206** if the integral DC power source is shut off or exhausted **230**. Shutting the DC power source off after a time period to prevent it from being exhausted is also not an essential aspect of the disclosed embodiments of the present invention. The feature is the subject matter of co-pending U.S. Patent Application, number ~~<to be inserted>~~ 10/644,683, entitled “Automatic Shut Off of DC Power Source in the Extended Absence of AC Power”, and filed contemporaneously. For further details, see the co-pending application.

On Page 12, lines 3-8, please replace the paragraph with the following:

Suppressing or ignoring power button and device wake events when AC is absent, is also not an essential aspect of the disclosed embodiments of the present

invention. The feature is the subject matter of co-pending U.S. Patent Application, number ~~<to be inserted>~~ 10/644,628, entitled "Power button and Device wake events Processing Methods in the Absence of AC Power", and filed contemporaneously.

On Page 15, lines 1-5, please replace the paragraph with the following:

In one embodiment where automatic shut off of backup power source **132** after a period of time is supported, in block **308**, BIOS **124** also sets up an arrangement to subsequently shut off backup power source **132** after elapse of the time period, before completing the OS write to register **122** of MCH/ICH/BB **108**. See copending application ~~<insert number>~~ 10/644,683 for further details.